

Head_pin	Name	\$PINS	ADDR/OFFSET	GPIO NO	Mode7	Mode6	Mode5	Mode4	Mode3	Mode2	Mode1	Mode0	PIN	default	Notes
P8_01	DGND														Ground
P8_02	DGND														Ground
P8_03	GPIO1_6	6	0x818/018	38	gpio1[6]						mmc1_dat6	gpmc_ad6	R9	31	Used on Board (Group: pinmux_emmc2_pins)
P8_04	GPIO1_7	7	0x81c/01c	39	gpio1[7]						mmc1_dat7	gpmc_ad7	T9	31	Used on Board (Group: pinmux_emmc2_pins)
P8_05	GPIO1_2	2	0x808/008	34	gpio1[2]						mmc1_dat2	gpmc_ad2	R8	31	Used on Board (Group: pinmux_emmc2_pins)
P8_06	GPIO1_3	3	0x80c/00c	35	gpio1[3]						mmc1_dat3	gpmc_ad3	T8	31	Used on Board (Group: pinmux_emmc2_pins)
P8_07	TIMER4	36	0x890/090	66	gpio2[2]					timer4		gpmc_advn_ale	R7	37	
P8_08	TIMER7	37	0x894/094	67	gpio2[3]					timer7		gpmc_oen_ren	T7	37	
P8_09	TIMER5	39	0x89c/09c	69	gpio2[5]					timer5		gpmc_be0n_cle	T6	37	
P8_10	TIMER6	38	0x898/098	68	gpio2[4]					timer6		gpmc_wen	U6	37	
P8_11	GPIO1_13	13	0x834/034	45	gpio1[13]			eQEP2B_in	mmc2_dat1	mmc1_dat5	lcd_data18	gpmc_ad13	R12	27	pull down
P8_12	GPIO1_12	12	0x830/030	44	gpio1[12]			EQEP2A_IN	MMC2_DAT0	MMC1_DAT4	LCD_DATA19	GPMC_AD12	T12	27	pull down
P8_13	EHRPWM2B	9	0x824/024	23	gpio0[23]			ehrpwm2B	mmc2_dat5	mmc1_dat1	lcd_data22	gpmc_ad9	T10	27	pull down
P8_14	GPIO0_26	10	0x828/028	26	gpio0[26]			ehrpwm2_tripzone_in	mmc2_dat6	mmc1_dat2	lcd_data21	gpmc_ad10	T11	27	pull down
P8_15	GPIO1_15	15	0x83c/03c	47	gpio1[15]			eQEP2_strobe	mmc2_dat3	mmc1_dat7	lcd_data16	gpmc_ad15	U13	27	pull down
P8_16	GPIO1_14	14	0x838/038	46	gpio1[14]			eQEP2_index	mmc2_dat2	mmc1_dat6	lcd_data17	gpmc_ad14	V13	27	pull down
P8_17	GPIO0_27	11	0x82c/02c	27	gpio0[27]			ehrpwm0_synco	mmc2_dat7	mmc1_dat3	lcd_data20	gpmc_ad11	U12	27	pull down
P8_18	GPIO2_1	35	0x88c/08c	65	gpio2[1]	mcasp0_fsr			mmc2_clk	gpmc_wait1	lcd_memory_clk	gpmc_clk_mux0	V12	27	pull down
P8_19	EHRPWM2A	8	0x820/020	22	gpio0[22]			ehrpwm2A	mmc2_dat4	mmc1_dat0	lcd_data23	gpmc_ad8	U10	27	pull down
P8_20	GPIO1_31	33	0x884/084	63	gpio1[31]					mmc1_cmd	gpmc_be1n	gpmc_csn2	V9	32	Used on Board (Group: pinmux_emmc2_pins)
P8_21	GPIO1_30	32	0x880/080	62	gpio1[30]					mmc1_clk	gpmc_clk	gpmc_csn1	U9	32	Used on Board (Group: pinmux_emmc2_pins)
P8_22	GPIO1_5	5	0x814/014	37	gpio1[5]						mmc1_dat5	gpmc_ad5	V8	31	Used on Board (Group: pinmux_emmc2_pins)
P8_23	GPIO1_4	4	0x810/010	36	gpio1[4]						mmc1_dat4	gpmc_ad4	U8	31	Used on Board (Group: pinmux_emmc2_pins)
P8_24	GPIO1_1	1	0x804/004	33	gpio1[1]						mmc1_dat1	gpmc_ad1	V7	31	Used on Board (Group: pinmux_emmc2_pins)
P8_25	GPIO1_0	0	0x800/000	1	gpio1[0]						mmc1_dat0	gpmc_ad0	U7	31	Used on Board (Group: pinmux_emmc2_pins)
P8_26	GPIO1_29	31	0x87c/07c	61	gpio1[29]							gpmc_csn0	V6	37	pull UP
P8_27	GPIO2_22	56	0x8e0/0e0	86	gpio2[22]						gpmc_a8	lcd_vsync	U5	0	Allocated (Group: nxp_hdmi_bonelt_pins)
P8_28	GPIO2_24	58	0x8e8/0e8	88	gpio2[24]						gpmc_a10	lcd_pclk	V5	0	Allocated (Group: nxp_hdmi_bonelt_pins)
P8_29	GPIO2_23	57	0x8e4/0e4	87	gpio2[23]						gpmc_a9	lcd_hsync	R5	0	Allocated (Group: nxp_hdmi_bonelt_pins)
P8_30	GPIO2_25	59	0x8ec/0ec	89	gpio2[25]						gpmc_a11	lcd_ac_bias_en	R6	0	Allocated (Group: nxp_hdmi_bonelt_pins)
P8_31	UART5_CTSN	54	0x8d8/0d8	10	gpio0[10]	uart5_ctsn		uart5_rxd	mcasp0_axr1	eQEP1_index	gpmc_a18	lcd_data14	V4	8	Allocated (Group: nxp_hdmi_bonelt_pins)
P8_32	UART5_RTSN	55	0x8dc/0dc	11	gpio0[11]	uart5_rtsn		mcasp0_axr3	mcasp0_ahclkx	eQEP1_strobe	gpmc_a19	lcd_data15	T5	8	Allocated (Group: nxp_hdmi_bonelt_pins)
P8_33	UART4_RTSN	53	0x8d4/0d4	9	gpio0[9]	uart4_rtsn		mcasp0_axr3	mcasp0_fsr	eQEP1B_in	gpmc_a17	lcd_data13	V3	8	Allocated (Group: nxp_hdmi_bonelt_pins)
P8_34	UART3_RTSN	51	0x8cc/0cc	81	gpio2[17]	uart3_rtsn		mcasp0_axr2	mcasp0_ahclkx	ehrpwm1B	gpmc_a15	lcd_data11	U4	8	Allocated (Group: nxp_hdmi_bonelt_pins)
P8_35	UART4_CTSN	52	0x8d0/0d0	8	gpio0[8]	uart4_ctsn		mcasp0_axr2	mcasp0_aclkr	eQEP1A_in	gpmc_a16	lcd_data12	V2	8	Allocated (Group: nxp_hdmi_bonelt_pins)
P8_36	UART3_CTSN	50	0x8c8/0c8	80	gpio2[16]	uart3_ctsn			mcasp0_axr0	ehrpwm1A	gpmc_a14	lcd_data10	U3	8	Allocated (Group: nxp_hdmi_bonelt_pins)
P8_37	UART5_TXD	48	0x8c0/0c0	78	gpio2[14]	uart2_ctsn		uart5_txd	mcasp0_aclkr	ehrpwm1_tripzone_in	gpmc_a12	lcd_data8	U1	8	Allocated (Group: nxp_hdmi_bonelt_pins)
P8_38	UART5_RXD	49	0x8c4/0c4	79	gpio2[15]	uart2_rtsn		uart5_rxd	mcasp0_fsx	ehrpwm0_synco	gpmc_a13	lcd_data9	U2	8	Allocated (Group: nxp_hdmi_bonelt_pins)
P8_39	GPIO2_12	46	0x8b8/0b8	76	gpio2[12]				eQEP2_index		gpmc_a6	lcd_data6	T3	8	Allocated (Group: nxp_hdmi_bonelt_pins)
P8_40	GPIO2_13	47	0x8bc/0bc	77	gpio2[13]			pr1_edio_data_out7	eQEP2_strobe		gpmc_a7	lcd_data7	T4	8	Allocated (Group: nxp_hdmi_bonelt_pins)
P8_41	GPIO2_10	44	0x8b0/0b0	74	gpio2[10]				eQEP2A_in		gpmc_a4	lcd_data4	T1	8	Allocated (Group: nxp_hdmi_bonelt_pins)
P8_42	GPIO2_11	45	0x8b4/0b4	75	gpio2[11]				eQEP2B_in		gpmc_a5	lcd_data5	T2	8	Allocated (Group: nxp_hdmi_bonelt_pins)
P8_43	GPIO2_8	42	0x8a8/0a8	72	gpio2[8]				ehrpwm2_tripzone_in		gpmc_a2	lcd_data2	R3	8	Allocated (Group: nxp_hdmi_bonelt_pins)
P8_44	GPIO2_9	43	0x8ac/0ac	73	gpio2[9]				ehrpwm0_synco		gpmc_a3	lcd_data3	R4	8	Allocated (Group: nxp_hdmi_bonelt_pins)
P8_45	GPIO2_6	40	0x8a0/0a0	70	gpio2[6]				ehrpwm2A		gpmc_a0	lcd_data0	R1	8	Allocated (Group: nxp_hdmi_bonelt_pins)
P8_46	GPIO2_7	41	0x8a4/0a4	71	gpio2[7]				ehrpwm2B		gpmc_a1	lcd_data1	R2	8	Allocated (Group: nxp_hdmi_bonelt_pins)

使用中変更不可能(機能を殺す必要があり)
初期値GPIO ブルアップ
将来的に使うと便利そうなピン機能