

Head_pin	Name	\$PINS	ADDR/OFFSET	GPIO NO	Mode7	Mode6	Mode5	Mode4	Mode3	Mode2	Mode1	Mode0	PIN	default	Notes	
P9_01	GND														Ground	
P9_02	GND														Ground	
P9_03	DC_3.3V														250mA Max Current	
P9_04	DC_3.3V														250mA Max Current	
P9_05	VDD_5V														1A Max Current (only if DC jack powered)	
P9_06	VDD_5V														1A Max Current (only if DC jack powered)	
P9_07	SYS_5V														250mA Max Current	
P9_08	SYS_5V														250mA Max Current	
P9_09	PWR_BUT														Has a 5V Level (pulled up by TPS65217C)	
P9_10	SYS_RESETn											RESET_OUT	A10			
P9_11	UART4_RXD	28	0x870/070	30	gpio0[30]	uart4_rxd_mux2		mmc1_sdcd	rmii2_crs_dv	gpmc_csn4	mii2_crs	gpmc_wait0	T17	37	NB: GPIOs limit current to 4-6mA output	pull UP
P9_12	GPIO1_28	30	0x878/078	60	gpio1[28]	mcasp0_aclkr_mux3		gpmc_dir	mmc2_dat3	gpmc_csn6	mii2_col	gpmc_be1n	U18	37	and approx. 8mA on input.	pull UP
P9_13	UART4_TXD	29	0x874/074	31	gpio0[31]	uart4_txd_mux2		mmc2_sdcd	rmii2_rxerr	gpmc_csn5	mii2_rxerr	gpmc_wpn	U17	37		pull UP
P9_14	EHRPWM1A	18	0x848/048	50	gpio1[18]	ehrpwm1A_mux1		gpmc_a18	mmc2_dat1	rgmii2_td3	mii2_txd3	gpmc_a2	U14	27		pull down
P9_15	GPIO1_16	16	0x840/040	48	gpio1[16]	ehrpwm1_tripzone_input		gpmc_a16	mii2_txen	rmii2_tctl	gmii2_txen	gpmc_a0	R13	27		pull down
P9_16	EHRPWM1B	19	0x84c/04c	51	gpio1[19]	ehrpwm1B_mux1		gpmc_a19	mmc2_dat2	rgmii2_td2	mii2_txd2	gpmc_a3	T14	27		pull down
P9_17	I2C1_SCL	87	0x95c/15c	5	gpio0[5]				ehrpwm0_syncl	I2C1_SCL	mmc2_sdwp	spi0_cs0	A16	62		
P9_18	I2C1_SDA	86	0x958/158	4	gpio0[4]				ehrpwm0_tripzone	I2C1_SDA	mmc1_sdwp	spi0_d1	B16	62		
P9_19	I2C2_SCL	95	0x97c/17c	13	gpio0[13]			spi1_cs1	I2C2_SCL	dcan0_rx	timer5	uart1_rtsn	D17	73	Allocated (Group: pinmux_i2c2_pins)	
P9_20	I2C2_SDA	94	0x978/178	12	gpio0[12]			spi1_cs0	I2C2_SDA	dcan0_tx	timer6	uart1_ctsn	D18	73	Allocated (Group: pinmux_i2c2_pins)	
P9_21	UART2_TXD	85	0x954/154	3	gpio0[3]	EMU3_mux1			ehrpwm0B	I2C2_SCL	uart2_txd	spi0_d0	B17	37		pull UP
P9_22	UART2_RXD	84	0x950/150	2	gpio0[2]	EMU2_mux1			ehrpwm0A	I2C2_SDA	uart2_rxd	spi0_sclk	A17	37		pull UP
P9_23	GPIO1_17	17	0x844/044	49	gpio1[17]	ehrpwm0_synco		gpmc_a17	mmc2_dat0	rgmii2_rxdv	gmii2_rxdv	gpmc_a1	V14	27		pull down
P9_24	UART1_TXD	97	0x984/184	15	gpio0[15]				I2C1_SCL	dcan1_rx	mmc2_sdwp	uart1_txd	D15	37		pull UP
P9_25	GPIO3_21	107	0x9ac/1ac	117	gpio3[21]			EMU4_mux2	mcasp1_axr1	mcasp0_axr3	eQEP0_strobe	mcasp0_ahclkx	A14	30	Allocated (Group: mcasp0_pins)	
P9_26	UART1_RXD	96	0x980/180	14	gpio0[14]				I2C1_SDA	dcan1_tx	mmc1_sdwp	uart1_rxd	D16	37		pull UP
P9_27	GPIO3_19	105	0x9a4/1a4	115	gpio3[19]			EMU2_mux2	mcasp1_fsx	mcasp0_axr3	eQEP0B_in	mcasp0_fsr	C13	27		pull down
P9_28	SPI1_CS0	103	0x99c/19c	113	gpio3[17]				eCAP2_in_PWM2_out	spi1_cs0	ehrpwm0_syncl	mcasp0_ahclkx	C12	2	Allocated (Group: mcasp0_pins)	
P9_29	SPI1_D0	101	0x994/194	111	gpio3[15]				mmc1_sdcd_mux1	spi1_d0	ehrpwm0B	mcasp0_fsx	B13	10	Allocated (Group: mcasp0_pins)	
P9_30	SPI1_D1	102	0x998/198	112	gpio3[16]				mmc2_sdcd_mux1	spi1_d1	ehrpwm0_tripzone	mcasp0_axr0	D12	27		pull down
P9_31	SPI1_SCLK	100	0x990/190	110	gpio3[14]				mmc0_sdcd_mux1	spi1_sclk	ehrpwm0A	mcasp0_aclkx	A13	0	Allocated (Group: mcasp0_pins)	
P9_32	VADC														Voltage Reference for ADC (NB: 1.8V)	
P9_33	AIN4												C8		NB: 1.8V tolerant	
P9_34	AGND														Ground for ADC	
P9_35	AIN6												A8		NB: 1.8V tolerant	
P9_36	AIN5												B8		NB: 1.8V tolerant	
P9_37	AIN2												B7		NB: 1.8V tolerant	
P9_38	AIN3												A7		NB: 1.8V tolerant	
P9_39	AIN0												B6		NB: 1.8V tolerant	
P9_40	AIN1												C7		NB: 1.8V tolerant	
P9_41A	CLKOUT2	109	0x9b4/1b4	20	gpio0[20]	EMU3_mux0		timer7_mux1	clkout2	tc1kin		xdma_event_intr1	D14	27	Both signals are connected to P21 of P11	
P9_41B	GPIO3_20		0x9a8/1a8	116	gpio3[20]			emu3	Mcasp1_axr0		eQEP0_index	mcasp0_axr1	D13		Both signals are connected to P21 of P11	
P9_42A	GPIO0_7	89	0x964/164	7	gpio0[7]	xdma_event_intr2		mmc0_sdwp	spi1_sclk	pr1_ecap0_ecap_capin_apwm_o	spi1_cs1	uart3_txd	C18	27	Both signals are connected to P21 of P11	
P9_42B	GPIO3_18		0x9a0/1a0	114	gpio3[18]				Mcasp1_aclkx	Mcasp0_axr2	eQEP0A_in	Mcasp0_aclkr	B12		Both signals are connected to P21 of P11	
P9_43	GND														See Pg.50 of the SRM	
P9_44	GND														Ground	
P9_45	GND														Ground	
P9_46	GND														Ground	

使用中変更不可能(機能を殺す必要があり)
初期値GPIO プルアップ
将来的に使うと便利そうなピン機能